

J1017 U.S. PTO
09/884778
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	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE	
SCANNED <i>hp</i>	<i>ed 3</i>	Q.A.	<i>Am</i>

APPLICATION NO. 09/884778	CONT/PRIOR	CLASS 38724	SUBCLASS 262	ART UNIT 2887	EXAMINER A. VV
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TITLE	APPLICANTS
1. <u>General</u>	
2. <u>Specific</u>	
3. <u>Other</u>	

Richard Hall
How Lin
Christopher Majka
Matthew Seward

Dinh

Method and apparatus to establish circuit layers

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. 6	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED 	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)				
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
			ISSUE BATCH NUMBER		

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